Electronic Acknowledgement Receipt					
EFS ID:	7592776				
Application Number:	10586305				
International Application Number:					
Confirmation Number:	2516				
Title of Invention:	MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE HAVING ELECTRODES ON OPPOSED SIDES OF AN IC ELEMENT THEREOF AND FIRST AND SECOND CIRCUIT LAYERS ELECTRICALLY CONNECTED TO THE ELECTRODES				
First Named Inventor/Applicant Name:	Kousuke Tanaka				
Customer Number:	20457				
Filer:	William Ivan Solomon/Kelli Harris				
Filer Authorized By:	William Ivan Solomon				
Attorney Docket Number:	1204.46401X00				
Receipt Date:	11-MAY-2010				
Filing Date:	14-JUL-2006				
Time Stamp:	18:45:14				
Application Type:	U.S. National Stage under 35 USC 371				
Payment information:	_1				
Submitted with Payment	yes				
Payment Type	Credit Card				
Payment was successfully received in RAM	\$130				
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File Listing:					

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1 Amendment After Final			113103		
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Warnings:					
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2 Extension of Time	46401X00-1MoEOT.pdf	63724	no	1	
	extension of filme	46401X00-1M0E01.pui	b06eef9cda7ef8c62782a541abd7eSba471 2027a	110	
Warnings:					
Information:					
3 Fee Worksheet (PTO-875)	fee-info.pdf	30823	no	2	
	rec violance (i 10 0/3)	lee-iiio.pai	680d872a72db4a74b1895b559b11db5281 4a47c2		_
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New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810). a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.